

SMPS MOSFET

IRF7455

HEXFET® Power MOSFET

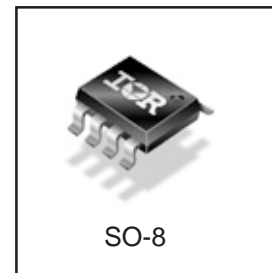
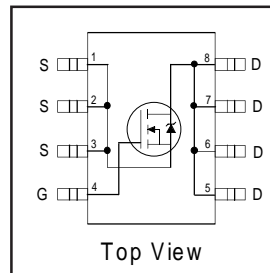
Applications

- High Frequency DC-DC Converters with Synchronous Rectification

V_{DS}	R_{DS(on)} max	I_D
30V	0.0075Ω	15A

Benefits

- Ultra-Low R_{DS(on)} at 4.5V V_{GS}
- Low Charge and Low Gate Impedance to Reduce Switching Losses
- Fully Characterized Avalanche Voltage and Current



Absolute Maximum Ratings

Symbol	Parameter	Max.	Units
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-to-Source Voltage	± 12	V
I _D @ T _A = 25°C	Continuous Drain Current, V _{GS} @ 10V	15	A
I _D @ T _A = 70°C	Continuous Drain Current, V _{GS} @ 10V	12	
I _{DM}	Pulsed Drain Current ^①	120	
P _D @ T _A = 25°C	Maximum Power Dissipation ^③	2.5	W
P _D @ T _A = 70°C	Maximum Power Dissipation ^③	1.6	W
	Linear Derating Factor	0.02	W/°C
T _J , T _{STG}	Junction and Storage Temperature Range	-55 to + 150	°C

Thermal Resistance

	Parameter	Max.	Units
R _{θJA}	Maximum Junction-to-Ambient ^④	50	°C/W

Typical SMPS Topologies

- Telecom 48V Input Converters with Logic-Level Driven Synchronous Rectifiers

Notes ① through ④ are on page 8
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Static @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	30	—	—	V	V _{GS} = 0V, I _D = 250μA
ΔV _{(BR)DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient	—	0.029	—	V/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance	—	0.0060	0.0075	Ω	V _{GS} = 10V, I _D = 15A ④
		—	0.0069	0.009		V _{GS} = 4.5V, I _D = 12A ④
		—	0.010	0.020		V _{GS} = 2.8V, I _D = 3.5A ④
V _{GS(th)}	Gate Threshold Voltage	0.6	—	2.0	V	V _{DS} = V _{GS} , I _D = 250μA
I _{DSS}	Drain-to-Source Leakage Current	—	—	20	μA	V _{DS} = 24V, V _{GS} = 0V
		—	—	100		V _{DS} = 24V, V _{GS} = 0V, T _J = 125°C
I _{GSS}	Gate-to-Source Forward Leakage	—	—	200	nA	V _{GS} = 12V
	Gate-to-Source Reverse Leakage	—	—	-200		V _{GS} = -12V

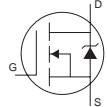
Dynamic @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
g _{fs}	Forward Transconductance	44	—	—	S	V _{DS} = 10V, I _D = 15A
Q _g	Total Gate Charge	—	37	56	nC	I _D = 15A
Q _{gs}	Gate-to-Source Charge	—	8.9	13		V _{DS} = 24V
Q _{gd}	Gate-to-Drain ("Miller") Charge	—	13	20		V _{GS} = 5.0V, ③
t _{d(on)}	Turn-On Delay Time	—	17	—		V _{DD} = 15V
t _r	Rise Time	—	18	—	ns	I _D = 1.0A
t _{d(off)}	Turn-Off Delay Time	—	51	—		R _G = 6.0Ω
t _f	Fall Time	—	44	—		V _{GS} = 4.5V ③
C _{iss}	Input Capacitance	—	3480	—	pF	V _{GS} = 0V
C _{oss}	Output Capacitance	—	870	—		V _{DS} = 25V
C _{rss}	Reverse Transfer Capacitance	—	100	—		f = 1.0MHz

Avalanche Characteristics

	Parameter	Typ.	Max.	Units
E _{AS}	Single Pulse Avalanche Energy②	—	200	mJ
I _{AR}	Avalanche Current①	—	15	A
E _{AR}	Repetitive Avalanche Energy①	—	0.25	mJ

Diode Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I _S	Continuous Source Current (Body Diode)	—	—	2.5	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I _{SM}	Pulsed Source Current (Body Diode) ①	—	—	120		
V _{SD}	Diode Forward Voltage	—	—	1.2	V	T _J = 25°C, I _S = 2.5A, V _{GS} = 0V ③
t _{rr}	Reverse Recovery Time	—	64	96	ns	T _J = 25°C, I _F = 2.5A
Q _{rr}	Reverse Recovery Charge	—	99	150	nC	di/dt = 100A/μs ③

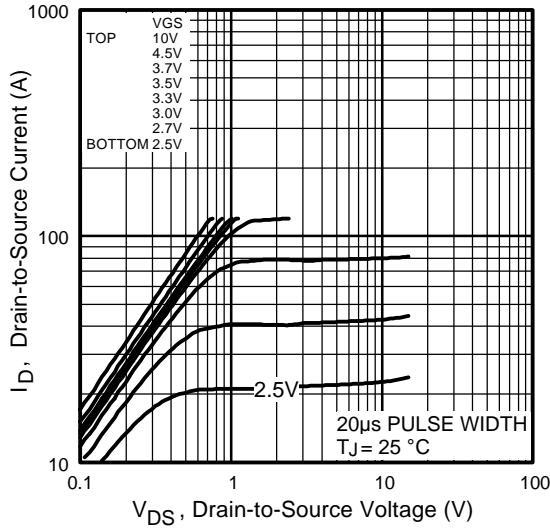


Fig 1. Typical Output Characteristics

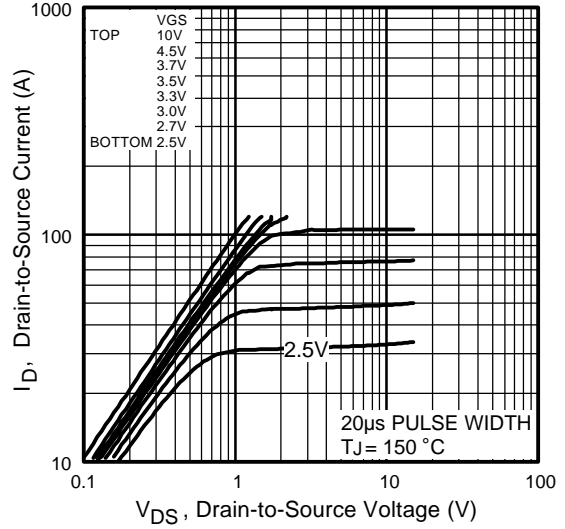


Fig 2. Typical Output Characteristics

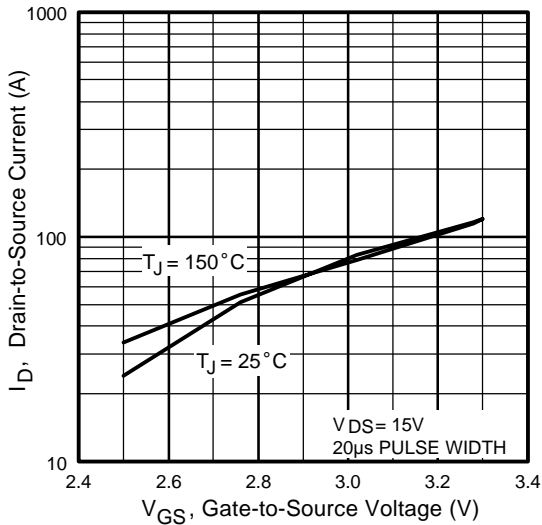


Fig 3. Typical Transfer Characteristics

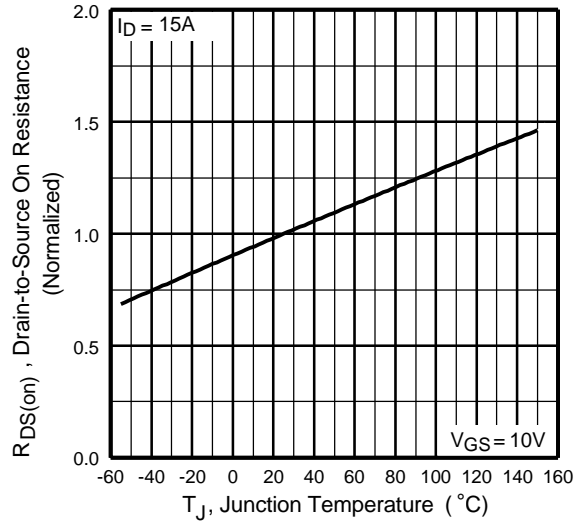


Fig 4. Normalized On-Resistance Vs. Temperature

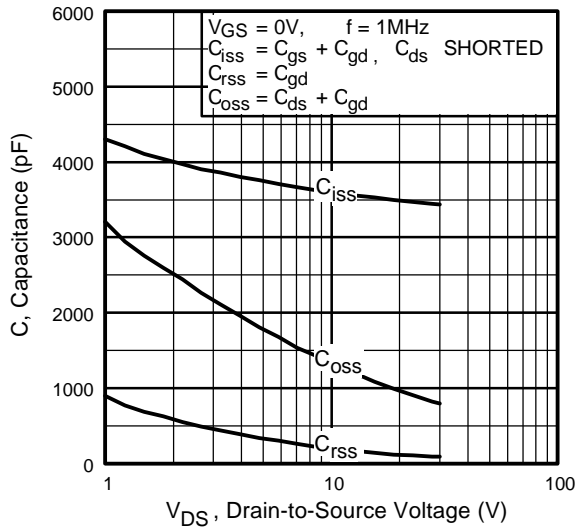


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

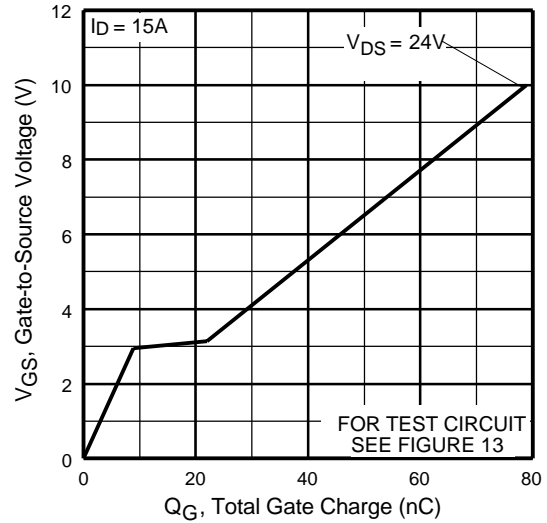


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

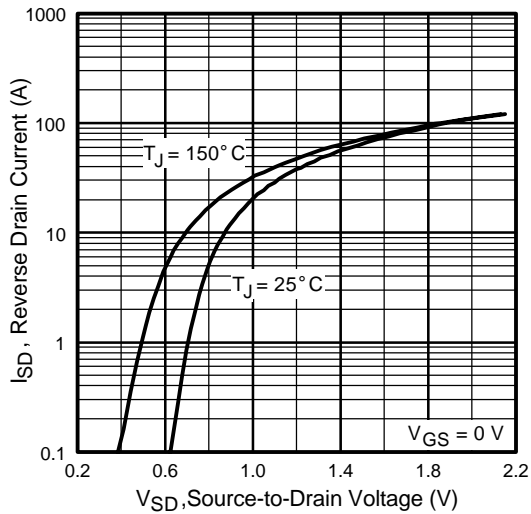


Fig 7. Typical Source-Drain Diode Forward Voltage

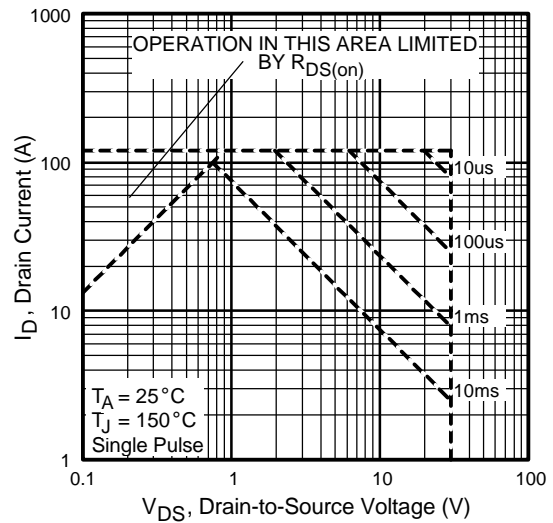


Fig 8. Maximum Safe Operating Area

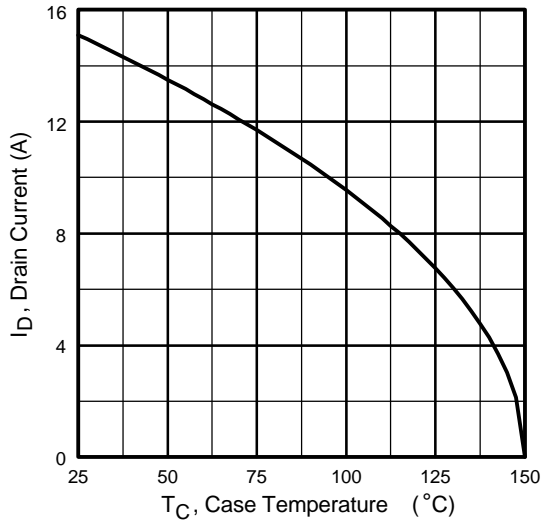


Fig 9. Maximum Drain Current Vs. Case Temperature

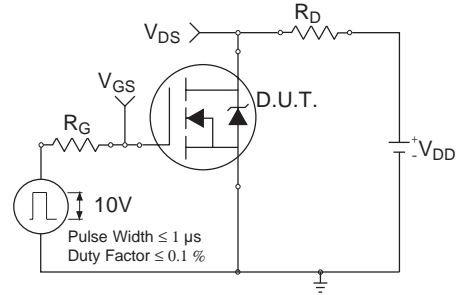


Fig 10a. Switching Time Test Circuit

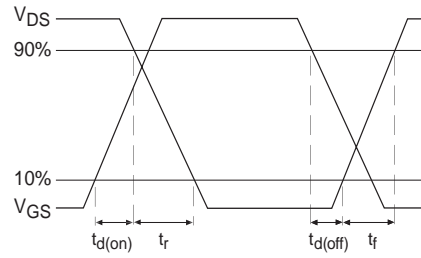


Fig 10b. Switching Time Waveforms

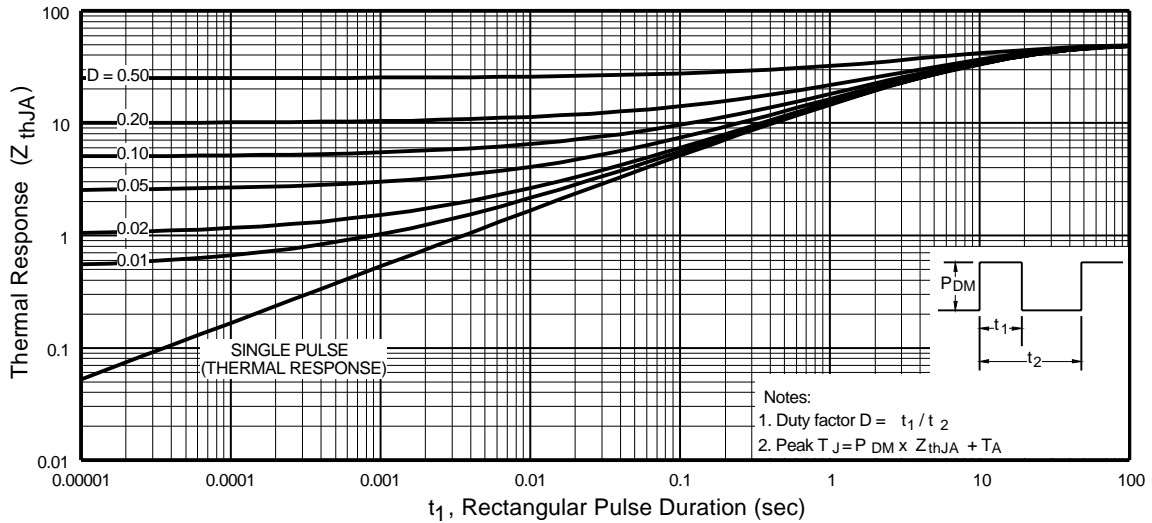


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

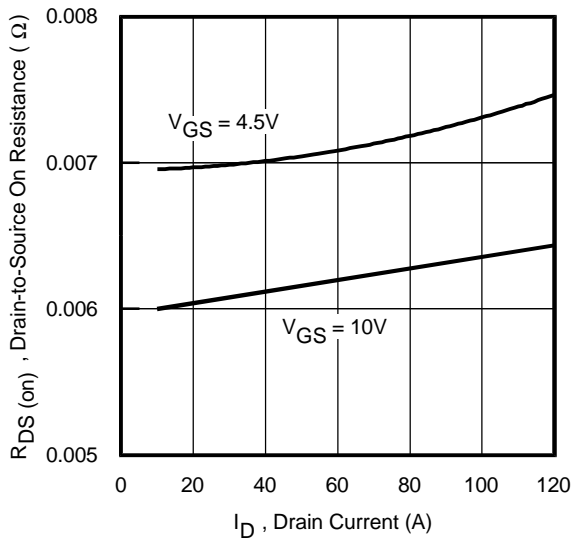


Fig 12. On-Resistance Vs. Drain Current

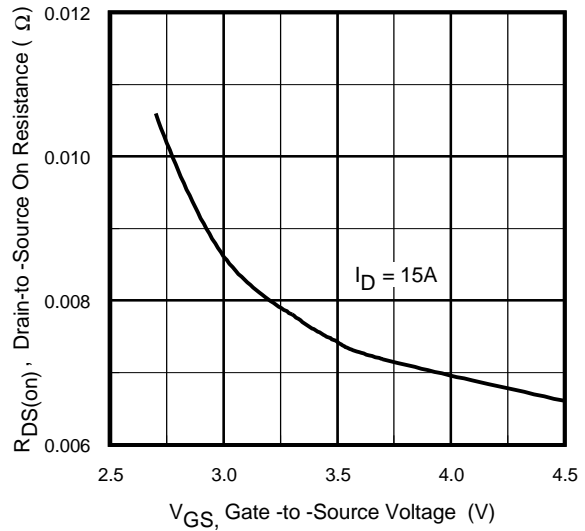


Fig 13. On-Resistance Vs. Gate Voltage

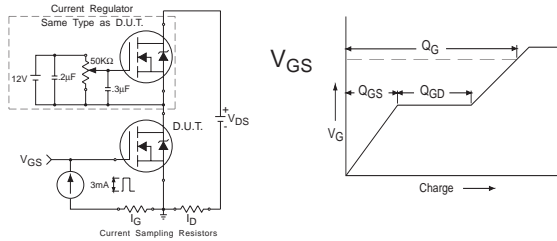


Fig 13a&b. Basic Gate Charge Test Circuit and Waveform

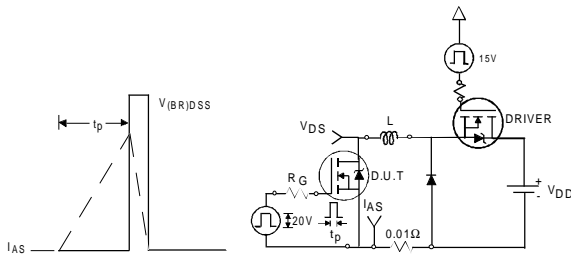


Fig 14a&b. Unclamped Inductive Test circuit and Waveforms

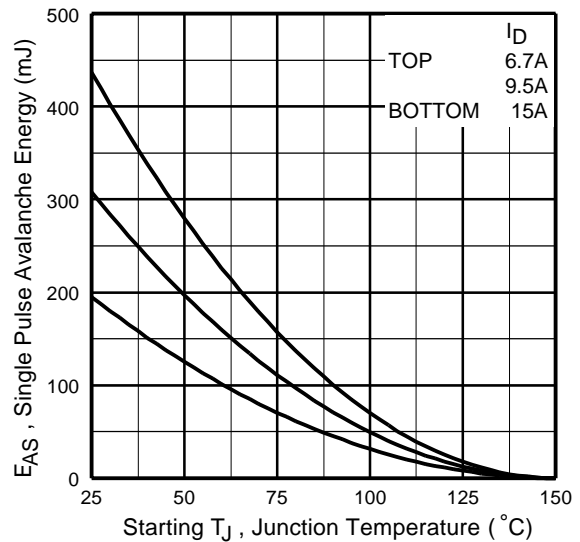
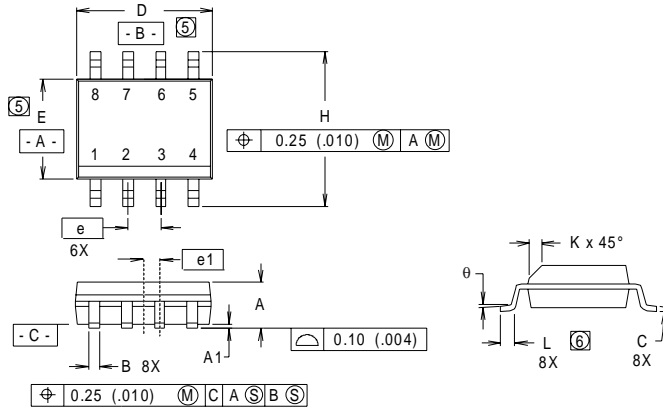


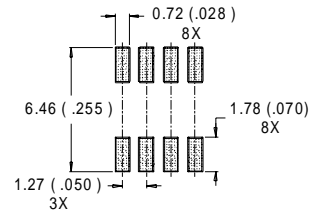
Fig 14c. Maximum Avalanche Energy Vs. Drain Current

SO-8 Package Details



DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
B	.014	.018	0.36	0.46
C	.0075	.0098	0.19	0.25
D	.189	.196	4.80	4.98
E	.150	.157	3.81	3.99
e	.050 BASIC		1.27 BASIC	
e1	.025 BASIC		0.635 BASIC	
H	.2284	.2440	5.80	6.20
K	.011	.019	0.28	0.48
L	0.16	.050	0.41	1.27
θ	0°	8°	0°	8°

RECOMMENDED FOOTPRINT

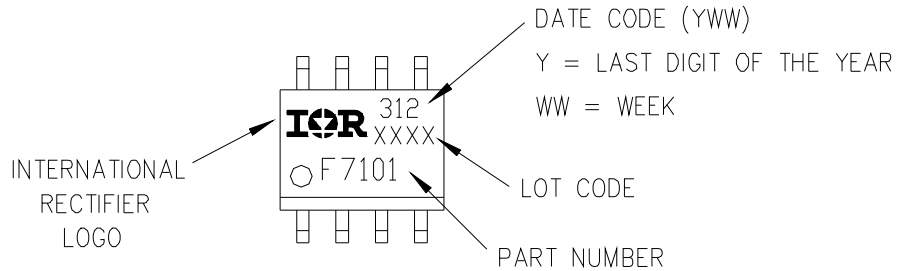


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M-1982.
2. CONTROLLING DIMENSION : INCH.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
- ⑤ DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS
MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.006).
- ⑥ DIMENSIONS IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE..

SO-8 Part Marking

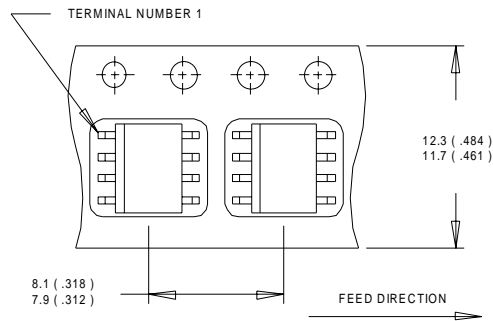
EXAMPLE: THIS IS AN IRF7101



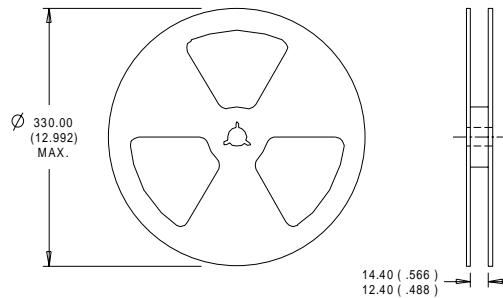
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SO-8 Tape and Reel



- NOTES:
1. CONTROLLING DIMENSION : MILLIMETER.
 2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
 3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



- NOTES:
1. CONTROLLING DIMENSION : MILLIMETER.
 2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting $T_J = 25^\circ\text{C}$, $L = 1.8\text{mH}$
 $R_G = 25\Omega$, $I_{AS} = 15\text{A}$.
- ③ Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.
- ④ When mounted on 1 inch square copper board, $t < 10$ sec

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IR GERMANY: Saalburgstrasse 157, 61350 Bad Homburg Tel: ++ 49 (0) 6172 96590
IR ITALY: Via Liguria 49, 10071 Borgaro, Torino Tel: ++ 39 011 451 0111
IR JAPAN: K&H Bldg., 2F, 30-4 Nishi-Ikebukuro 3-Chome, Toshima-Ku, Tokyo 171 Tel: 81 (0)3 3983 0086
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Data and specifications subject to change without notice. 4/00